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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/823,307	04/13/2004	Cho Dong-Sik	8729-234 (CW9052US/SY)	4759
22150	7590	05/14/2007	EXAMINER	
F. CHAU & ASSOCIATES, LLC 130 WOODBURY ROAD WOODBURY, NY 11797			NGUYEN, LEON VIET Q	
			ART UNIT	PAPER NUMBER
			2611	
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**Please find below and/or attached an Office communication concerning this application or proceeding.**

The time period for reply, if any, is set in the attached communication.

<b>Office Action Summary</b>	Application No. 10/823,307	Applicant(s) DONG-SIK, CHO	
	Examiner Leon-Viet Q. Nguyen	Art Unit 2611	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 29 March 2007.
- 2a) ☐ This action is **FINAL**.                      2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-43 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-6, 8-14 and 16-43 is/are rejected.
- 7) ☒ Claim(s) 7, 8, 15, 21, and 34 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 13 April 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All    b) ☐ Some \* c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)            | 4) <input type="checkbox"/> Interview Summary (PTO-413)           |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)   | Paper No(s)/Mail Date. _____                                      |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date <u>7/17/06</u> .   | 6) <input type="checkbox"/> Other: _____                          |

## **DETAILED ACTION**

### ***Priority***

1. Receipt is acknowledged of papers submitted under 35 U.S.C. 119(a)-(d), which papers have been placed of record in the file.

### ***Information Disclosure Statement***

2. The information disclosure statement (IDS) submitted on 7/17/2006 was filed after the mailing date of 7/17/2006. The submission is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

### ***Drawings***

3. Figures 1 and 2 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the

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applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

### ***Claim Objections***

1. Claims 8, 21, and 34 are objected to because of the following informalities:
  - a. In claim 8, "The receiver of claims 1" should read "The receiver of claim 1".
  - b. In claim 21, "the fractional reduction" lacks antecedent basis.
  - c. In claim 34, "The receiver of claim 33" lacks antecedent basis.

Appropriate correction is required.

### ***Claim Rejections - 35 USC § 112***

1. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
2. **Claims 18 and 33 recite the limitation "correlating the I and Q digital signals with *the expected codes*". There is insufficient antecedent basis for this limitation in the claim.**

### ***Claim Rejections - 35 USC § 101***

3. 35 U.S.C. 101 reads as follows:

Whoever invents or discovers any new and useful process, machine, manufacture, or composition of matter, or any new and useful improvement thereof, may obtain a patent therefor, subject to the conditions and requirements of this title.

**4. Claim 33 is rejected under 35 U.S.C. 101 because the claimed invention is directed to non-statutory subject matter.**

Claim 33 pertains solely to stored codes that are not embodied in any computer-readable media.

*"Similarly, computer programs claimed as computer listings per se, i.e., the descriptions or expressions of the programs, are not physical "things." They are neither computer components nor statutory processes, as they are not "acts" being performed. Such claimed computer programs do not define any structural and functional interrelationships between the computer program and other claimed elements of a computer which permit the computer program's functionality to be realized. In contrast, a claimed computer-readable medium encoded with a computer program is a computer element which defines structural and functional interrelationships between the computer program and the rest of the computer which permit the computer program's functionality to be realized, and is thus statutory. See Lowry, 32F.3d at 1583-84, 32 USPQ2d at 1035. See Interim Guidelines on 35 USC 101, Annex IV (a): Functional Descriptive Material.*

***Claim Rejections - 35 USC § 103***

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the

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invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

**2. Claims 1, 3, 5, 6, 9, 11, 13, 14, 16, 18, 20, 22, 24, 25, 27, 29, 30, 31, 33, 35-41, and 43 are rejected under 35 U.S.C. 103(a) as being unpatentable over the background of applicant's specification (hereby referred to as the background) and further in view of Rabinowitz et al (US20030901932).**

Re claim 1, the background teaches a global positioning system (GPS) receiver, comprising:

a converter for converting received GPS signals to in-phase and quadrature-phase digital signals (digital IF in fig. 2);

a correlator (correlator 13 in fig. 2) for generating expected codes and correlating the I and Q digital signals with the expected codes to output sampled I values and sampled Q values for a tap (¶0011);

However the background fails to teach:

a filter for filtering the sampled I values and sampled Q values to modified I values to each of the modified Q values, and for adding each of the modified I values to each of the corresponding modified Q values of the tap, and for outputting a count for sum which is positive;

a counter for incrementing a counter value upon each count received from the filter; and

a comparator for comparing the counter value to a threshold value upon completion of measure of values of the tap for determining the presence of a peak.

Rabinowitz teaches a filter for filtering the sampled I values and sampled Q values to modified I values to each of the modified Q values (LPFs 410I and 410Q in fig. 7), and for adding each of the modified I values to each of the corresponding modified Q values of the tap (summer 508 in fig. 9), and for outputting a count for sum which is positive (correlator output in fig. 9, it would be obvious to one of ordinary skill in the art to output a positive sum);

a counter for incrementing a counter value upon each count received from the filter (bank of counters 1210 in fig. 12); and

a comparator for comparing the counter value to a threshold value upon completion of measure of values of the tap for determining the presence of a peak (comparator 1220 in fig. 12).

Therefore taking the combined teachings of the background and Rabinowitz as a whole, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the filter, counter, and comparator of Rabinowitz into the GPS receiver of the background. The motivation to combine Rabinowitz and the background would be to recover satellite positioning system aiding information from selected data segments (§0020).

Re claim 3, the modified invention of the background teaches a receiver wherein the modified I values and modified Q values (the outputs of filters 410I and 410Q in fig.

7 of Rabinowitz) are fractional reductions of respective sampled I values and sampled Q values (Although not explicitly taught, it is well known to one of ordinary skill in the art that signals sent through a low pass filter are reduced by some factor. Therefore the filtered I and Q signals as taught by Rabinowitz are interpreted to be fractionally reduced), the fractional reduction being the same for both the sampled I values and the sampled Q values (LPFs 410I and 410Q in fig. 7 of Rabinowitz are interpreted to be the same, therefore the fractional reduction for both I and Q signals would be the same).

Re claim 5, the modified invention of the background teaches a receiver of further including a memory for storing sampled I and Q values of the tap (memory 15 in fig. 2 of the background) found to have a counter value exceeding the threshold ( $\lceil 0100$  in Rabinowitz, it would have been obvious to one of ordinary skill in the art to store those samples which have values exceeding a threshold), wherein sampled I and Q values of other taps are not stored in the memory (although not explicitly stated, one of ordinary skill in the art would have found it obvious to discard the unnecessary values).

Re claim 6, the modified invention of the background teaches a receiver further including a domain transformer for performing domain transform on data stored in the memory (FFT unit 20 in fig. 2 of the background).



Re claim 9, the background teaches a global positioning system (GPS) receiver, comprising:

a converter for converting received GPS signals to in-phase and quadrature-phase digital signals (digital IF in fig. 2);

a correlator (correlator 13 in fig. 2) for generating expected codes and correlating the I and Q digital signals with the expected codes to output sampled I values and sampled Q values for a tap (110011);

a memory for storing the sum of each of the modified I values and corresponding modified Q values of the tap having a counter value exceeding the threshold (memory 15 in fig. 2).

However the background fails to teach:

a filter for filtering the sampled I values and sampled Q values to modified I values and modified Q values, and for adding each of the modified I values to each of the corresponding modified Q values of the tap, and for outputting a count for each sum which is positive;

a counter for incrementing a counter value upon each count received from the filter; and

a comparator for comparing the counter value to a threshold value upon completion of measure of values of the tap for determining the presence of a peak.

Rabinowitz teaches a filter for filtering the sampled I values and sampled Q values to modified I values to each of the modified Q values (LPFs 410I and 410Q in fig. 7), and for adding each of the modified I values to each of the corresponding modified Q values of the tap (summer 508 in fig. 9), and for outputting a count for sum which is positive (correlator output in fig. 9, it would be obvious to one of ordinary skill in the art to output a positive sum);

a counter for incrementing a counter value upon each count received from the filter (bank of counters 1210 in fig. 12); and

a comparator for comparing the counter value to a threshold value upon completion of measure of values of the tap for determining the presence of a peak (comparator 1220 in fig. 12).

Therefore taking the combined teachings of the background and Rabinowitz as a whole, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the filter, counter, and comparator of Rabinowitz into the GPS receiver of the background. The motivation to combine Rabinowitz and the background would be to recover satellite positioning system aiding information from selected data segments (§0020).

Re claim 11, the limitations have been analyzed and addressed with respect to claim 3.

Re claim 13, the modified invention of the background teaches a receiver of further including a domain transformer for performing domain transform on data stored in the memory (FFT unit 20 in fig. 2 of the background).

Re claim 14, the modified invention of the background teaches a receiver wherein the domain transformer is a Fast Fourier Transformer (FFT unit 20 in fig. 2 of the background).

Re claim 16, the modified invention of the background teaches a receiver wherein the memory further stores the sampled I and Q values of the tap (memory 15 in fig. 2 of the background storing I and Q values from correlator 13) identified as having a peak (¶0091 in Rabinowitz. The correlator output having multiple correlation peaks).

Re claim 18, the limitations have been analyzed and addressed with respect to claim 1. It would be obvious and necessary to have a method of using the GPS receiver as claimed in claim 1.

Re claim 20, the limitations have been analyzed and addressed with respect to claim 3.

Re claim 22, the limitations have been analyzed and addressed with respect to claim 5.

Re claim 24, the limitations have been analyzed and addressed with respect to claim 6.

Re claim 25, the modified invention of the background teaches a method further including storing the sum of each of the modified I values and corresponding modified Q values of the tap (memory 15 in fig. 2 of the background, ¶0011. The correlated I and Q data are integrated and stored in memory 15. Furthermore it is well know that integration is merely a process of summation) found to have a counter value exceeding the threshold (¶0100 in Rabinowitz, the counter being incremented for the time interval in which the correlator output exceeds a threshold).

Re claim 27, the limitations have been analyzed and addressed with respect to claim 3.

Re claim 29, the modified invention of the background teaches a method of further including storing in the memory the sampled I and Q values of the tap (memory 15 in fig. 2 of the background storing I and Q values from correlator 13) found to have a peak (¶0091 in Rabinowitz. The correlator output having multiple correlation peaks), wherein sampled I and Q values of taps not found to have a peak are not stored in the memory (although not explicitly stated, one of ordinary skill in the art would have found it obvious to discard the unnecessary values).

Re claim 30, the limitations have been analyzed and addressed with respect to claim 13.

Re claim 31, the limitations have been analyzed and addressed with respect to claim 14.

Re claim 33, the limitations have been analyzed and addressed with respect to claim 1. It would be obvious and necessary to have a stored program device having stored codes to perform the method of using the GPS receiver as claimed in claim 1.

Re claim 35, the limitations have been analyzed and addressed with respect to claim 3.

Re claim 36, the limitations have been analyzed and addressed with respect to claim 22.

Re claim 37, the limitations have been analyzed and addressed with respect to claim 24.

Re claim 38, the limitations have been analyzed and addressed with respect to claim 25.

Re claim 39, the limitations have been analyzed and addressed with respect to claim 3.

Re claim 40, the limitations have been analyzed and addressed with respect to claim 29.

Re claim 41, the limitations have been analyzed and addressed with respect to claim 30.

Re claim 43, the modified invention of the background teaches a stored program device wherein the stored program device is one of a flash memory or ROM (¶0140 in Rabinowitz).

**3. Claims 2, 10, 19, and 34 are rejected under 35 U.S.C. 103(a) as being unpatentable over the background of applicant's specification (hereby referred to as the background) and Rabinowitz et al (US20030901932) and further in view of Woo (US6259401).**

Re claim 2, the modified invention of the background fails to teach a receiver wherein the sampled I values and sampled Q values are modified by assigning a

positive value to the sampled I value or sampled Q value when a present sample I value or Q value has a different sign from the immediately prior sample I value or sample Q value. However Woo teaches a receiver wherein the sampled I values and sampled Q values (col. 15 lines 15-20) are modified by assigning a positive value to the sampled I value or sampled Q value (col. 15 lines 20-22, the samples are sign adjusted and made positive) when a present sample I value or Q value has a different sign from the immediately prior sample I value or sample Q value (col. 15 lines 15-20).

Therefore taking the modified teachings of the background and Rabinowitz with Woo as a whole, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the sign correcting of Woo into the GPS receiver of the background and Rabinowitz. The motivation to combine Woo, Rabinowitz, and the background would be to reduce resolution error in pseudorange tracking (col. 15 lines 11-15).

Re claim 10, the limitations have been analyzed and addressed with respect to claim 2.

Re claim 19, the limitations have been analyzed and addressed with respect to claim 2.

Re claim 34, the limitations have been analyzed and addressed with respect to claim 2.



**4. Claims 4, 12, 21, and 28 are rejected under 35 U.S.C. 103(a) as being unpatentable over the background of applicant's specification (hereby referred to as the background) and Rabinowitz et al (US20030901932) and further in view of Fullerton et al (US5963581).**

Re claim 4, the modified invention of the background fails to teach a receiver wherein the fractional reduction is one half. However Fullerton teaches teach a receiver including a filter wherein the fractional reduction is one half (col. 21 lines 14-17).

Therefore taking the modified teachings of the background and Rabinowitz with Fullerton as a whole, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the filter of Fullerton into the GPS receiver of the background and Rabinowitz. The motivation to combine Fullerton, Rabinowitz, and the background would be to maximize the signal-to-noise ratio of a data stream (col. 21 lines 13-14).

Re claim 12, the limitations have been analyzed and addressed with respect to claim 4.

Re claim 21, the limitations have been analyzed and addressed with respect to claim 4.

Re claim 28, the limitations have been analyzed and addressed with respect to claim 4.

**5. Claims 8, 17, 23, 26, 32, and 42 are rejected under 35 U.S.C. 103(a) as being unpatentable over the background of applicant's specification (hereby referred to as the background) and Rabinowitz et al (US20030901932) and further in view of Lennen (US6888879).**

Re claim 8, the modified invention of the background fails to teach a receiver wherein the memory is one of a SRAM or a DRAM. However Lennen teaches using SRAM or DRAM physical memory (col. 11 lines 35-41).

Therefore taking the modified teachings of the background and Rabinowitz with Lennen as a whole, it would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the SRAM or DRAM of Lennen into the GPS receiver of the background and Rabinowitz. The motivation to combine Lennen, Rabinowitz, and the background would be to allow higher clock rates (col. 11 lines 38-39) or use less surface area in an integrated circuit (col. 11 lines 39-40).

Re claim 17, the limitations have been analyzed and addressed with respect to claim 8.

Re claim 23, the limitations have been analyzed and addressed with respect to claim 8.

Re claim 26, the limitations have been analyzed and addressed with respect to claim 8.

Re claim 32, the limitations have been analyzed and addressed with respect to claim 8.

Re claim 42, the limitations have been analyzed and addressed with respect to claim 8.

***Allowable Subject Matter***

6. Claims 7 and 15 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.


***Conclusion***

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Leon-Viet Q. Nguyen whose telephone number is 571-270-1185. The examiner can normally be reached on monday-friday, alternate friday off, 7:30AM-5PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David C. Payne can be reached on 571-272-3024. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Leon-Viet Nguyen/

  
DAVID C. PAYNE  
SUPERVISORY PATENT EXAMINER